

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application for:

Bhaskar P. Chatterjee, et al.

Application No.: 10/676,276

Filed: 09/30/2003

Patent No.: 7,362,621 B2

Issued: 04/22/2008

For: REGISTER FILE WITH A  
SELECTABLE KEEPER CIRCUIT

Examiner: Sofocleous, Alexander

Art Group: 2824

Confirmation No. 6564

Attn: Certificate of Corrections Branch  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REQUEST FOR CERTIFICATE OF CORRECTION**

Dear Sir:

Upon review of the above-referenced Letters Patent, Applicants noted errors that are the mistakes of the United States Patent Office. The errors are as follows:

**Column 5**

Line 9, [Claim 1.] "... comprising; ..." should read --..., comprising: ...--.

Line 14, [Claim 1.] "... driving signal; and" should read --...driving signal trace; and--.

Lines 20-24, [Claim 1.] "... wherein ... is designed to:

determine whether one of said plurality of select signal traces,  
corresponding to the evaluated bit trace, is asserted; and  
conditionally decouple ..."

should read "... wherein ... is designed to:

determine whether one of said plurality of local bit traces evaluates;

determine if one of said plurality of select signal traces, corresponding to the evaluated bit trace, is asserted; and

conditionally decouple ...-.

Lines 63-65, [Claim 8.] "... couple to ..." should read "...coupled to...-.

#### Column 6

Lines 36-37, "17. A system comprising:

a processor having:

a register file including:

a register file output signal trace;

a driving signal trace; and

a global bit trace ..."

should read --17. A system comprising:

a processor having:

a register file output signal trace;

a driving signal trace; and

a register file including:

a global bit trace ...-.

Included with this submission is a Certificate of Correction and it is respectfully requested that a Certificate of Correction be issued. This request for correction is made under the provisions of 37 CFR 1.323.

Respectfully submitted,  
SCHWABE, WILLIAMSON & WYATT, P.C.

Dated: July 24, 2009

/Angela M. Sagalewicz/  
Angela M. Sagalewicz  
Reg. No. 56,113

Pacwest Center, Suites 1500-2000  
1211 SW Fifth Avenue  
Portland, Oregon 97204  
Telephone: 503-222-9981

# **UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION**

Page 1 of 2

PATENT NO. : 7,362,621 B2  
 APPLICATION NO.: 10/676,276  
 ISSUE DATE : 04/22/2008  
 INVENTOR(S) : Bhaskar P. Chatterjee, et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

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Lines 63-65, [Claim 8.] "... couple to ..." should read —...coupled to...—.

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: **Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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should read --17. A system comprising:

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